Docket No.: L&L-I0232

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : WOLFGANG RÖSNER ET AL.

Filed : CONCURRENTLY HEREWITH

Title : METHOD FOR PRODUCING A VERTICAL SEMICONDUCTOR

TRANSISTOR COMPONENT AND A VERTICAL SEMICONDUCTOR

TRANSISTOR COMPONENT

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks, Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

U.S. Patent 5,871,870 (Alwan), dated February 16, 1999;

German published Non-Prosecuted Patent Application DE 42 35 152 A1 (Winkler et al.), dated April 21, 1994, method for fabricating a semiconductor fine structure and semiconductor components produced thereby, for example vertical transistors;

Patent Abstracts of Japan 08 306 905 A (Hiroshi), dated November 22, 1996;

International Search Report, dated July 16, 1999;

International Search Report, dated January 31, 2001.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,

Date: January 16, 2002

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U.S. DEPARTMENT OF COMMERCE				Attorney Docket No.: L&L-10232 Appl. No.											
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))				Applicant WOLFGANG RÖSNER ET AL. Filing Date January 16, 2002 Group Art Unit											
								EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
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		FOREIG	N PATEN	NT DOCUMENT											
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL YES NO								
	J	42 35 152 A1	4/21/94	Germany											
	K	08 306 905 A	11/22/96	Japan											
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